

IMAGE FORMING APPARATUS

BACKGROUND OF THE INVENTION

Field of the Invention

5 The present invention relates to an image forming apparatus, and more particularly to techniques for correcting position difference among a plurality of images.

Related Background Art

10 A color image forming apparatus is known which has a plurality of record units each applying a laser beam modulated by record data to a photosensitive drum, developing an electrostatic latent image on a photosensitive member by an electronic photographing
15 process and transferring the image to a transfer sheet to form a color image by sequentially transporting the transfer sheet by a transfer belt to each record unit and superposing each color image on the transfer sheet.

20 With the color image forming apparatus of this type, each electrostatic latent image on each photosensitive drum is developed and transferred to a transfer sheet on a transfer belt. In this case, registration of each color image is shifted if there is a mechanical mount error of each photosensitive drum,
25 or an optical path length error or an optical path change of each laser beam.

 In order to solve this problem, so-called

registration correction techniques have been proposed. According to the techniques, a registration correction pattern image formed on a transfer belt is read with a CCD sensor or the like to detect a registration shift
5 of each color photosensitive drum. In accordance with this registration shift, electrical correction of image signals to be recorded is performed and mechanical correction of the optical path length or optical path is performed by driving each reflection mirror disposed
10 in the intermediate optical path of each laser beam.

The image forming apparatus of this type is, however, associated with some problem in registration correction that the read level of each registration correction pattern may become different and hence the
15 detection level of each registration correction pattern may become different. This problem results from a different image density of each registration correction pattern recorded by the record unit, i.e., a different composition of coloring transfer material and the like.

20 If electrical correction of image signals to be recorded is performed and mechanical correction of the optical path length or optical path is performed by driving each reflection mirror disposed in the intermediate optical path of each laser beam, in
25 accordance with the registration shift amount obtained by detecting each registration correction mark recorded by the record unit, then high precision adjustment is

difficult because the detected registration shift amount itself may contain some error.

SUMMARY OF THE INVENTION

5 It is an object of the invention to solve the above-described problem.

 It is another object of the invention to provide high precision registration correction.

10 In order to achieve the above object, according to one aspect of the present invention, there is provided an image forming apparatus comprising: a plurality of image forming means each having an image carrier and forming an image on the image carrier; mark detecting means for detecting a plurality of registration
15 correction marks formed on a recording medium by the plurality of the image forming means; correcting means for correcting a position difference between images formed by the plurality of the image forming means in accordance with a detection result by the mark
20 detecting means; and controlling means for independently controlling an image forming operation of each of the plurality of the image forming means so that an image density of each of the registration correction marks formed by the plurality of the image
25 forming means has a different predetermined image density.

 Other objects and features of the present

invention will become apparent from the following detailed description of embodiments when read in conjunction with the accompanying drawings.

5 BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a schematic diagram showing the structure of an image forming apparatus according to an embodiment of the invention.

10 Figs. 2A and 2B are diagrams showing the structure of a laser scan optical system of the image forming apparatus shown in Fig. 1.

Figs. 3A and 3B are timing charts showing pattern image write timings of the image forming apparatus shown in Fig. 1.

15 Fig. 4 is a schematic diagram showing pattern images formed on a transfer belt in response to each enable signal shown in Figs. 3A and 3B.

20 Fig. 5 is a block diagram showing the structure of an image processing station and interface shown in Fig. 1.

Fig. 6 is a diagram showing a control unit shown in Fig. 1.

25 Fig. 7 is a diagram showing the main structure of a video controller in the image processing station shown in Fig. 1.

Fig. 8 is a diagram showing the main structure of a registration controller shown in Fig. 6.

Fig. 9 is a timing chart illustrating the operation timings of the registration controller shown in Fig. 8.

Fig. 10 is a histogram of an image on the transfer belt shown in Fig. 1.

Fig. 11 is a flow chart illustrating a registration correction process to be executed by the image forming apparatus shown in Fig. 1.

Fig. 12 is a diagram showing another example of the structure of the video controller in the image processing station shown in Fig. 1.

Fig. 13 is a diagram showing the main structure of a laser driver shown in Fig. 12.

Figs. 14A, 14B and 14C are timing charts showing the operation timings of a circuit shown in Fig. 13.

Fig. 15 is a memory map of a storage medium for storing various data processing programs which can be read by the image forming apparatus of this invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

Embodiments of the invention will be described.

Fig. 1 is a schematic diagram showing the structure of an image forming apparatus according to an embodiment of the invention.

Referring to Fig. 1, a transfer belt 1 is moved in the direction indicated by an arrow Z shown in the central area in Fig. 1, as a pulse motor M15 is driven

and its rotation force is transmitted to a drive roller 42. Reference numerals 2 to 5 represent magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) photosensitive drums, respectively disposed in this order.

5 Semiconductor lasers 39Ma, 39Cy, 39Ye and 39Bk of respective colors are driven by laser drivers 38Ma, 38Cy, 38Ye and 38Bk and emit laser beams L1 and L1'; L2 and L2'; L3 and L3'; and L4 and L4' to scan the photosensitive drums 2 to 5 and form electrostatic
10 latent images on the photosensitive drums 2 to 5. Drum motors M11 to M14 rotate the photosensitive drums 2 to 5 by predetermined rotation amounts.

Image stations ST1 to ST4 for magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) colors respectively
15 disposed in this order mechanically correct a shift of the magnification factor and inclination of registration by using reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk which are driven by pulse motors M1 to M8.

20 A reader unit 10 includes illumination lamps 6a and 6b, illumination lamps 7a and 7b, converging lenses 8a and 8b, reflection mirrors 9a and 9b, CCD sensors 10a and 10b and the like. A pair of front and back patterns (e.g., cross marks having a predetermined
25 width) formed on the transfer belt 1 moved by the pulse motor 15 is illuminated and its reflection light is focussed upon the CCD sensors 10a and 10b to read the

patterns. In this embodiment, infrared emission lamps are used as the illumination lamps 6a and 6b; and 7a and 7b. This is because the reflection light amount of each color pattern is intended to be made as constant as possible in view of the fact that the reflection light amount of each color toner does not depend too much upon the coloring components of toner.

In accordance with pattern data read with the CCD sensors 10a and 10b, a control unit 51 collectively controls the formation of images, including predetermined data processing and mirror control for magnification and inclination adjustments, by communicating with an image processing station 52 via a CPU bus $\beta 500$. The image processing station 52 executes the predetermined data processing and mirror control for magnification and inclination adjustments, and communicates with an interface (I/F) 53. The interface (I/F) 53 can communicate with the image processing station 52 via a bus $\beta 501$ and with an external bus via a bus $\beta 502$.

The operation of each constituent element will be described.

First, an image forming operation will be described.

The photosensitive drums 2 to 5 corresponding to magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) are rotated by the drum motors M11 to M14 and uniformly

charged by a charge unit (not shown). The
photosensitive drums 2 to 5 corresponding to magenta
(Ma), cyan (Cy), yellow (Ye) and black (Bk) are exposed
by the laser beams L1 and L1'; L2 and L2'; L3 and L3';
5 and L4 and L4' optically modulated with video signals
to form electrostatic latent images which are developed
by a developing unit (not shown) to form visible
images.

The electrostatic latent images formed on the
10 photosensitive drums 2 to 5 are transferred at
predetermined timings to a transfer sheet fed from a
paper feed unit (not shown) and electrostatically
absorbed on the transfer belt 1. The transfer sheet is
transported in the arrow direction by the pulse motor
15 M15, fixed by a fixing unit and then ejected.

Next, an operation of reading a registration
correction pattern image will be described.

Pattern images on the photosensitive drums 2 to 5
visualized by a registration correction pattern image
20 forming circuit are transferred to the transfer belt 1
at timings shown in Fig. 3 to be described later, and
transported in the arrow direction. The transported
pattern images are sequentially read by the CCD sensors
10a and 10b with an optical system constituted of the
25 illumination lamps 6a and 6b, illumination lamps 7a and
7b, converging lenses 8a and 8b, and reflection mirrors
9a and 9b.

In this embodiment, in order to realize a high speed apparatus, a two-beam laser scan method is incorporated as the laser optical system. This two-beam laser scanning will be described with reference to Figs. 2A and 2B.

Figs. 2A and 2B are schematic diagrams showing the structure of a laser scan optical system of the image forming apparatus shown in Fig. 1. Fig. 2A shows the structure of a polygon scanner 40 and semiconductor lasers 39Ma, 39Cy, 39Ye and 39Bk, and Fig. 2B is a schematic diagram showing a laser radiation state.

As shown in Fig. 2A, eight laser beams in total are radiated, including the laser beams L1 and L1'; L2 and L2'; L3 and L3'; and L4 and L4' corresponding to magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk). A latent image of each color is formed on the photosensitive drum by using two laser beams of L1 and L1'; L2 and L2'; L3 and L3'; or L4 and L4'.

Referring to Fig. 2B, P (shown as a circle) represents a pixel formed by laser scanning, ZP represents a main scanning line pitch, and W represents a main scanning width. The laser beams L1(LM) and L1'(LM'); L2(LC) and L2'(LC'); L3(LY) and L3'(LY'); or L4(LBK) and L4'(LBK') are scanned at the main scanning line pitch ZP. The motion direction of the transfer belt 1 is indicated by an arrow Z. With such a two-beam laser scan method, even if the transport speed of

a transfer member (traveling speed of a belt) is set to a high speed, the time taken to form an image on the transfer member or belt is halved. Therefore, restrictions on the rotation of a polygon scanner, the laser drive frequency and the like can be reduced considerably.

For example, in an image forming apparatus having an image forming performance of 30 images per one minute, if the two-beam laser scan method is adopted, the image forming performance is doubled to 60 images per one minute without modifying the apparatus.

A pattern forming means of this invention corresponds to a means for reading predetermined registration correction pattern data stored in an unrepresented ROM, forming pattern latent image pairs on the photosensitive drums 2 to 5 at predetermined positions different in the axial direction by scanning each pair of the laser beams LM and LM'; LC and LC'; LY and LY'; and LBK and LBK', developing the latent image pairs with color toners of magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk), and transferring the image pairs to the transfer belt 1. In this embodiment, a pair of pattern images is formed at predetermined positions in the width direction perpendicular to the transport direction of the transfer belt 1.

The reader unit 10 includes the illumination lamps 6a and 6b, illumination lamps 7a and 7b, converging

lenses 8a and 8b, reflection mirrors 9a and 9b, CCD sensors 10a and 10b and the like. A pair of patterns (e.g., cross marks having a predetermined width) formed on the transfer belt 1 moved by the pulse motor 15 is illuminated and its reflection light is focussed upon the CCD sensors 10a and 10b to read the patterns.

In this embodiment, infrared emission lamps are used as the illumination lamps 6a and 6b; and 7a and 7b. This is because the reflection light amount of each color pattern is intended to be made as constant as possible in view of the fact that the reflection light amount of each color toner does not depend too much upon the coloring components of toner.

In accordance with pattern data read with the CCD sensors 10a and 10b, the control unit 51 collectively controls the formation of images, including predetermined data processing and mirror control for magnification and inclination adjustments, by communicating with the image processing station 52 in which a CPU 52c (in Fig. 5 to be described later) executes a control program stored in a ROM or the like.

In the image forming apparatus constructed as above, a video controller 52a (in Fig. 5 to be described later) of the image processing station 52 controls in such a manner that when each image forming means forms a registration correction pattern on a transport member (transfer belt 1) at a predetermined

timing, the reader unit 10 starts reading the registration correction mark transferred to the transport member (transfer belt 1). A registration controller 20 (in Fig. 6 to be described later) of the control unit 51 executes a predetermined calculation of the read data, and stores the calculated results of each color in RAMs 603 and 604 (in Fig. 8 to be described later) of the registration controller 20. CPU 52c (in Fig. 5 to be described later) of the image processing station 20 analyzes the stored calculation results to mechanically or electrically correct each image station (ST1 to ST4).

In this embodiment, registration of each drum is made coincident as in the following. A shift of the magnification and inclination of registration is mechanically corrected by moving the reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk of the optical scan system (provided for each drum) by the pulse motors M1 to M8, and a beam scan timing is electrically corrected by a video memory controller 52d which controls the timing of reading image data from a video memory 52b.

Figs. 3A and 3B are timing charts showing pattern image write timings of the image forming apparatus shown in Fig. 1. Fig. 3A is a timing chart showing pattern image write timings of respective colors, and Fig. 3B is an enlarged view showing the waveform of enable signals.

In Figs. 3A and 3B, since the two-beam laser scan method is used as described earlier, two enable signals for registration correction patterns of each color are output as shown in the enlarged view of Fig. 3B showing an area indicated by *. Two enable signals each having the same pulse width and space ZP of one line, such as the signals S2 and S2'; S3 and S3'; S4 and S4'; and S5 and S5', can be output. By using eight enable signals of four colors x 2 beams, registration correction pattern images of respective colors are formed. Similar enable signals are also generated for a usual image forming operation. The Ye laser enable signals S4 and S4' are Ye patch drawing enable signals for Bk pattern images.

By using such enable signals, pattern images such as shown in Fig. 4 are formed.

Fig. 4 is a schematic diagram showing pattern images formed on the transfer belt 1 by using the enable signals shown in Figs. 3A and 3B.

In Fig. 4, A1 represents a first pattern image read area. First laser patterns LP1Ma, LP1Cy, LP1Ye and LP1Bk and second laser patterns LP1Ma', LP1Cy', LP1Ye' and LP1Bk' formed on the transfer belt 1 are read with the CCD sensor 10a shown in Fig. 1 when they pass through the first pattern image read area A1. A2 represents a second pattern image read area. First laser patterns LP2Ma, LP2Cy, LP2Ye and LP2Bk and second

laser patterns LP2Ma', LP2Cy', LP2Ye' and LP2Bk' formed on the transfer belt 1 are read with the CCD sensor 10b shown in Fig. 1 when they pass through the second pattern image read area A2.

5 First laser patch enable signals PH1Ye and PH2Ye and second laser patch enable signals PH1Ye' and PH2Ye' are Ye patch enable signals for first laser patterns LP1Bk and LP2Bk and second laser patterns LP1Bk' and LP2Bk'.

10 With reference to Fig. 5, the structures of the image processing station 52 and interface (I/F) 53 shown in Fig. 1 will be described. The image processing station 52 and interface 53 constitute a circuit for driving laser beams in accordance with
15 input image data and forming images.

 Fig. 5 is a block diagram showing the structures of the image processing station 52 and interface (I/F) 53 shown in Fig. 1. Similar constituent elements to those shown in Fig. 1 are represented by using
20 identical reference numerals.

 The video controller 52a of the image processing station 52 transmits the registration correction patterns and video signals for usual image data read from the video memory 52b via busses β 521a to each
25 laser driver, synchronously with a BD signal β 528 and an ITOP signal β 529. The video memory 52b stores therein registration correction pattern data and image

data such as conventional image data. The video memory controller 52d controls the image data read timing from the video memory 52b by using a control signal $\beta 509$ to thereby electrically correct the scan timing of each
5 laser beam.

CPU 52c has inner RAM, ROM and the like. CPU 52c executes the control program stored in ROM to perform predetermined data processing and mirror control for magnification and inclination adjustments. Therefore,
10 the optical path length and optical path of each laser beam are mechanically corrected and the scan timing of each laser beam is electrically corrected to make the registration of each drum be coincident. CPU 52c also controls the control unit 51, video controller 52a,
15 video memory controller 52d and interface 53, respectively via buses $\beta 500$, $\beta 503$, $\beta 504$ and $\beta 501a$.

An interface controller 53b of the interface (I/F) 53 controls an external interface 53a via a bus $\beta 510$. The external interface 53a can transmit the data
20 externally received via the bus $\beta 502$ directly to the video memory 52b via a data bus $\beta 501b$.

The operation of each constituent element will be described.

As shown in Fig. 5, video signals for forming an
25 image are supplied via the external bus $\beta 502$ (for an external interface, a general interface such as the general purpose interface bus (GPIB) may be used) to

the external interface 53a and stored in the video memory 52b of the image processing station 52 via the bus $\beta 501b$.

In this case, CPU 52c activates the interface
5 controller 53b via the CPU interface bus $\beta 501a$ to control the operation of the external interface 53a via a control bus $\beta 510$.

The video memory controller 52d executes the address control and read/write control of the video
10 memory 52b to read/write video data and form an image.

In this case, the video memory controller 52d is controlled by CPU 52c via the control bus $\beta 504$. The video memory 52b sends the video data to the video controller 52a so that the PWM modulated laser beams L1
15 and L1'; L2 and L2'; L3 and L3'; and L4 and L4'; described earlier are generated and latent images are formed on the photosensitive drums.

CPU 52c is connected to the control unit 51 via the CPU bus $\beta 500$ to receive registration shift data and
20 supply electrical and mechanical registration correction target data to the control unit 51. In this manner, CPU 52c collectively controls the registration correction of this invention.

Fig. 6 is a block diagram showing the structure of
25 the control unit 51 shown in Fig. 1. Similar constituent elements to those shown in Fig. 1 are represented by using identical reference numerals.

In Fig. 6, CCD drivers 18 and 19 generate clocks (such as transfer pulse, reset pulse and shift pulse) $\beta 591$ and $\beta 592$ necessary for driving the CCD sensors 10a and 10b in accordance with original oscillation clocks $\beta 507$ and $\beta 508$ supplied from a registration controller 20, and supplies them to the CCD sensors 10a and 10b.

The CCD drivers 18 and 19 execute processes such as amplification and A/D conversion of pattern image signals $\beta 593$ and $\beta 594$ read by the CCD sensors 10a and 10b, and supply the digital pattern image signals $\beta 505$ and $\beta 506$ to the registration controller 20.

The registration controller 20 executes a registration correction pattern recognition process for the digital image signals $\beta 505$ and $\beta 506$ supplied from the CCD drivers 18 and 19, stores a plurality of pattern recognition data sets in a memory (not shown). These data sets are sent to CPU 52c of the image processing station 52 via the CPU bus $\beta 500$. CPU 52c calculates a position difference of each color pattern, and in accordance with the calculation results, the write timings of the main and sub-scanning are controlled.

A mirror motor controller 21 sets a pulse motor drive pulse value (motor drive control signal) $\beta 511$ to a mirror motor driver 22 in accordance with the drive pulse data supplied from CPU 52c. The mirror motor driver 22 drives each motor in accordance with each set

of drive pulses $\beta 512a$ to $\beta 515a$, and $\beta 512b$ to $\beta 515b$ corresponding to the pulse motor drive values. In this manner, the positioning control for the reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk is performed to correct the optical path length and optical path of each laser beam and correct a shift of the magnification and inclination of registration of each color.

The operation of each constituent element will be described.

The CCD sensors 10a and 10b read the pattern images of respective colors formed as shown in Fig. 4 on the front and back sides relative to the transport direction of the transfer belt 1 shown in Fig. 1. The original oscillation clocks $\beta 507$ and $\beta 508$ are supplied from the registration controller 20 to the CCD drivers 18 and 19, and the generated clocks (transfer pulse, reset pulse, shift pulse and the like) $\beta 591$ and $\beta 592$ necessary for driving the CCD sensors 10a and 10b are supplied to the CCD sensors 10a and 10b.

The pattern image signals read by the CCD sensors 10a and 10b are subjected to processes such as amplification and A/D conversion by the CCD drivers 18 and 19. The converted digital pattern image signals $\beta 505$ and $\beta 506$ are supplied to the registration controller 20.

The pattern image signals of respective colors

received by the registration controller 20 are subjected to the registration correction pattern recognition process. A plurality of pattern recognition data sets are stored in a temporary memory and then supplied to CPU 52c via the CPU bus $\beta 500$. By using the position of a pattern image of a predetermined color as a reference, CPU 52a calculates a position shift of the pattern image of each of the other colors. In accordance with this calculation results, the video controller 52a controls the memory read timing of the registration correction pattern image and the video memory controller 52d controls the memory read timing of a conventional image, to thereby correct the color shift of each color.

In order to correct the optical path length and optical path of each laser beam and correct the magnification and inclination of registration of each color, CPU 52c sends the drive pulse data to the mirror motor controller 21 to control the inclination correction pulse motors M5 to M8 and magnification correction pulse motors M1 to M4, which motors drive the reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk disposed in the optical path. In accordance with this drive pulse data, the mirror motor controller 21 sets the pulse motor drive pulse signal $\beta 511$ to the mirror motor driver 22 to drive the motors by the drive pulses $\beta 512a$ to $\beta 515a$, and $\beta 512b$ to $\beta 515b$. In this

manner, the positioning control for the reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk is performed.

Fig. 7 is a circuit block diagram showing the main structure of the video controller 52a of the image processing station 52 shown in Fig. 1.

In Fig. 7, an enable signal generation circuit (H enable signal generation circuit) 27 generates H enable signals $\beta 516a$ and $\beta 516b$ for two laser beams for the registration correction pattern image signals, in response to a beam detect signal (BD) $\beta 528$ which is a sync signal for the main scanning signal obtained when the laser beam scans an area outside of a record area, and supplies the H enable signals to NAND gates 36a and 36b. In this case, one of the detection signals of the two beams or a synthesized signal of the detection signals of the two beams is used as BD $\beta 528$.

An enable signal generation circuit (V enable signal generation circuit) 28 for the sub-scanning direction generates independently V enable signals $\beta 517a$ and $\beta 517b$ for two laser beams for the registration correction pattern image signals of each color, in response to an input activation signal (ITOP) $\beta 529$ to be used for registration correction pattern images, and supplies the V enable signals to NAND gates 36b and 36a.

In accordance with the V enable signals $\beta 517a$ and $\beta 517b$ supplied from the V enable signal generation

circuit 28, an address counter 29 generates address signals $\beta 531a$ and $\beta 531b$ of pattern RAMs 30a and 30b for the next registration correction images. In accordance with these address signals, pattern images $\beta 518a$ and
5 $\beta 518b$ (in this embodiment, cross patterns) are read from the pattern RAMs 30a and 30b. A patch register 31 stores patch data input via the video controller bus $\beta 503$ (and formed in accordance with the registration correction pattern images).

10 Under control of CPU 52c, a register 35 outputs a selection signal $\beta 526$ supplied from the video controller bus $\beta 503$ to selectors 32a and 32b. The selection signal $\beta 526$ is used for selecting either the pattern image signal $\beta 518a$ read from the pattern RAM
15 30a or the patch data (patch register signal) 519 read from the patch register 31, and for selecting either the pattern image signal $\beta 518b$ read from the pattern RAM 30b or the patch data 519.

Under control of CPU 52c, a register 35 outputs a
20 selection signal $\beta 527$ to selectors 33a and 33b. The selection signal $\beta 527$ is used for selecting either the image signal $\beta 520a$ selected by the selector 32a or an image signal (video signal) $\beta 521a$ input from a first video line, and for selecting either the pattern image
25 signal $\beta 520b$ selected by the selector 32b or an image signal (video signal) $\beta 521b$ input from a second video line.

γ RAMs 34a and 34b receive the image signals $\beta 522a$ and $\beta 522b$ selected by the selectors 33a and 33b, and output γ -transformed image data $\beta 523a$ and $\beta 523b$ to the laser drivers 38a and 38b via gate circuits 37a and 37b as video signals $\beta 525a$ and $\beta 525b$. Namely, the image density can be changed by rewriting tables in γ RAMS 34a and 34b. Gate signals $\beta 524a$ and $\beta 524b$ are supplied to the gate circuits 37a and 37b via NAND gates 36a and 36b.

In accordance with the input video signals $\beta 525a$ and $\beta 525b$, the laser drivers 38a and 38b turn on and off the semiconductor lasers 39a and 39b to form latent images on the photosensitive drums 2 to 5 by using the optical scan system including the polygon scanner and reflection mirrors.

In this embodiment, as described above, the laser drivers of the two-beam laser scan structure are used. Therefore, two systems for forming pattern image data and patch data are used to drive the semiconductor lasers.

In this embodiment, the video controller 52a is constituted of four sets of the circuit shown in Fig. 7 respectively of magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) to form an image of each color. The image density of each of the four colors, magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk), can therefore be adjusted independently.

The operation of the circuit for one of Ma, Cy, Ye and Bk will be described.

The beam detect signal (BD) $\beta 528$, which is obtained when the laser beam scans the area outside of the record area and which is used as the sync signal for the main scanning signal, is supplied to the enable signal generation circuit (H enable signal generation circuit) 27 which in turn generates the H enable signals $\beta 516a$ and $\beta 516b$ for the two laser beams of the registration correction pattern image signals. In this case, one of the detection signals of the two beams or a synthesized signal of the detection signals of the two beams is used as BD $\beta 528$. The input activation signal (ITOP) $\beta 529$ for registration correction pattern images is supplied to the enable signal generation circuit (V enable signal generation circuit) 28 for the sub-scanning direction which in turn generates the V enable signals $\beta 517a$ and $\beta 517b$ for two laser beams of the registration correction pattern image signals of each color.

The H enable signals $\beta 516a$ and $\beta 516b$ and V enable signals $\beta 517a$ and $\beta 517b$ are supplied to the address counter 29 which in turn generates the address signals $\beta 531a$ and $\beta 531b$ of the pattern RAMs 30a and 30b for the next registration correction images. In accordance with these address signals, the pattern image signals $\beta 518a$ and $\beta 518b$ (in this embodiment, cross patterns)

are read from the pattern RAMs 30a and 30b.

The patch register 31 stores patch data input via the video controller bus $\beta 503$ and formed in accordance with the registration correction pattern images. This patch data signal $\beta 519$ and pattern image signals $\beta 518a$ and $\beta 518b$ for the two laser beams are input to the selectors 32a and 32b. The select signal $\beta 526$ is input to the selectors 32a and 32b so that the pattern image signals $\beta 518$ are output for magenta (Ma), cyan (Cy), and black (Bk).

For the pattern image of yellow (Ye), under control of the select signal $\beta 526$ supplied via the video controller bus $\beta 503$, the signals $\beta 520a$ and $\beta 520b$ of switching between the pattern image data for the two laser beams and patch data are supplied to the selectors 33a and 33b at predetermined timings shown in the timing chart of Figs. 3A and 3B. Video signals $\beta 521a$ and $\beta 521b$ are input to the selectors 33a and 33b.

If toner of a carbon black type is used as the black toner, the pattern image cannot be read because the reflection optical system using the carbon black absorbs light.

To solve this problem, at a predetermined time before one of the registration correction pattern images of color which reflects other colors (magenta, cyan and yellow) is formed (in this embodiment, yellow), a yellow solid pattern (patch) is formed on

the transfer belt 1, and then the registration correction pattern image of black is formed on the yellow patch.

In this mode of forming the pattern images and patch, the select signal $\beta 527$ is used for selecting the pattern images and patch. The selected pattern image data $\beta 522a$ and $\beta 522b$ are supplied to γ RAMs 34a and 34b. The γ -transformed image data $\beta 523a$ and $\beta 523b$ are supplied to the laser drivers 38a and 38b via the gate circuits 37a and 37b as the video signals $\beta 525a$ and $\beta 525b$. The γ RAMS 34a and 34b have the γ -tables whose contents can be rewritten in response to an input of a γ setting signal (γ rewrite signal) $\beta 530$ supplied from the register 35.

By utilizing this rewrite function, the contents of the γ -tables are partially rewritten so that the densities of the registration correction pattern images and patch can be changed in the registration correction mode. For example, if the pattern images and patch use 8-bit data and the data in the pattern RAMs 30a and 30b to be used for forming the pattern images and patch is FFh, then the data in the γ -tables at the highest address is changed so that the pattern images can be formed at an optimum image density.

The gate signals $\beta 524a$ and $\beta 524b$ are supplied to the gate circuits 37a and 37b via NAND gates 36a and 36b. In accordance with the input video signals $\beta 525a$

and $\beta 525b$ input to the laser drivers 38a and 38b, the laser drivers 38a and 38b change the duty ratio of ON/OFF to perform PWM modulation and turn on and off the semiconductor lasers 39a and 39b to form latent
5 images on the photosensitive drums 2 to 5 by using the optical scan system including the polygon scanner and reflection mirrors.

In this embodiment, as described earlier, the laser drivers of the two-beam laser scan structure are
10 used. Therefore, two systems for forming image data and patch data are used to drive the semiconductor lasers.

In this embodiment, under control of CPU 52c, the enable signals for the main and sub-scanning are
15 controlled via the video controller bus $\beta 503$ to thereby control the write position of each registration correction pattern image. For the write position control of usual image data, the video data address generation signal to be generated by the video memory
20 controller 52d is controlled under CPU 52c via the video memory controller bus $\beta 504$ in accordance with the BD signal $\beta 528$ and ITOP signal $\beta 529$ so that the timing of reading video data from the video memory 52b via the video memory control bus $\beta 509$ can be controlled.

25 Image data of four colors supplied from the external video data bus $\beta 501b$ via the external interface 53a is stored beforehand in the video memory

52b. The external interface 53a is controlled by the interface controller 53b which is controlled by CPU 52c via the interface bus $\beta 501a$, so that data write into the video memory 52b is controlled by the control bus $\beta 510$.

The video data stored in the video memory 52b is read at the timings allowing images of respective colors to be superposed correctly, in accordance with the color shift correction amounts calculated as described previously. The image is therefore formed precisely on a transfer sheet.

In this embodiment, although the pattern generation circuit is provided for each color, the pattern RAMs 30a and 30b and the like may be used in common for all colors.

With reference to Figs. 8 to 10, a process of calculating the position and shape of a pattern image of each color will be described.

Fig. 8 is a block diagram showing the structure of the registration controller 20 shown in Fig. 6.

Referring to Fig. 6, D-type flip-flops DF1 and DF2 receive pattern data of one sub-scanning line output from the CCD sensors 10a and 10b via a line L505, and output the pattern data to adders 601 and 602 synchronously with VCLK. The adders 601 and 602 add the CCD data supplied from the flip-flops DF1 and DF2 to DB outputs from RAMS 603 and 604 to be described

later. This data of one line is supplied to D-type flip-flops DF3 and DF4.

A bus/timing controller 607 transfers CPU data 16, CPU addresses 17 and the like to and from CPU 52c via the CPU bus β 500, and outputs various timing signals such as RAMWR1, RAMWR2 and EN, a bank select signal BANKSEL and an address signal ADR.

In accordance with a main scanning enable signal LEN output at a timing shown in Fig. 9 to be described later, the flip-flops DF3 and DF4 write the data of one line in RAMs 603 and 604 at the addresses determined by address counters 605 and 606, synchronously with the write signals RAMWR1 and RAMWR2.

The memories are enabled while the sub-scanning enable signal is output.

In this embodiment, the pattern recognition is performed in accordance with the accumulated pattern data of pixels of one line, which data is read in order to calculate the position and shape of the pattern image of each color in the main and sub-scanning directions.

Fig. 9 is a timing chart illustrating the operation of the pattern recognition process to be executed by the registration controller 20 shown in Fig. 8.

Fig. 10 is a histogram obtained from a pattern image transferred to the transfer belt 1 shown in Fig.

1.

The pattern recognition process to be executed by the registration controller shown in Fig. 8 will be described.

5 The accumulated data of the sub-scanning direction is formed as in the following. Pattern data of one sub-scanning line output from, e.g., the CCD sensor 10a, is initialized in response to a reset signal RES1. Thereafter, data of one line is obtained through
10 addition by the adder 602. In response to the main scanning enable signal LEN output at the timing shown in Fig. 9, the data of one line is written in RAM 604 at the address determined by the address counter 606, synchronously with the write signal RAMWR2. The
15 memories are enabled while the sub-scanning enable signal is output.

 The accumulated data of the main scanning direction is formed as in the following. Pattern data of one main scanning line is initialized in response to
20 a reset signal RES2. In accordance with the write signal RAMWR1 and a data direction switching signal RAMDIR, a read-modify-write operation is repeated for each pixel. The accumulated data of each pixel of each main scanning line in the adder 601 is stored in RAM
25 603.

Therefore, as shown in Fig. 10, the accumulated data of each color in the main scanning and sub-

scanning directions of a pattern image of each color is stored in RAMs 603 and 604. Two circuits for this pattern recognition process are provided in the registration controller 20 in correspondence to two CCD
5 sensors 10a and 10b.

The memory space is used separately by setting each color bank and each set bank to the RAM upper addresses by using a bank select signal BANKSEL.

As described earlier, for the pattern image of
10 each of yellow (Ye), magenta (Ma), cyan (Cy) and black (Bk) (however, since the image of Bk is formed on the Ye patch, it has an inverse pattern of the pattern shown in Fig. 9), the accumulated data HD and VD both in the main and sub-scanning directions shown in Fig.
15 10 is stored in RAMs 603 and 604. In accordance with the accumulated data HD and VD stored in RAMs 603 and 604, the center of peak of the accumulated data is calculated by CPU 52c by accessing the RAMs 603 and 604.

20 The calculated center position in the main and sub-scanning directions of each color is made coincident with the center position of the pattern image of other colors. To this end, the write positions in the main and sub-scanning directions are
25 controlled and at the same time, the positions of the reflection mirrors 1000Ma, 1000Cy, 1000Ye and 1000Bk are controlled by the mirror motor controller 21 which

controls the mirror driver 22 to drive the magnification (variable optical path length) correction motors M1 to M4 and inclination (variable optical path) correction motors M5 to M8. The motors are controlled
5 by CPU 52c via the bus β 500.

With reference to the flow chart of Fig. 11, the details of the registration correction process of this invention will be given.

It is generally known that the infrared
10 reflectivities of magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) toners are different because of the influence of coloring material components. The magnitudes of the reflectivities are defined by the relation of "Ma > Cy > Ye > Bk".

15 However, this relation cannot be defined definitely because the reflectivity difference is not so large and is influenced by coloring material components.

Although the reflectivity difference is not so
20 larger, there is some difference. It is known that this difference adversely affects the read precision of a registration correction pattern. Namely, the reflectivity difference results in a difference of the read peak level and read pattern of each color, which
25 generates an error of pattern position calculation so that there is a large possibility of a registration shift even after the registration correction.

In this embodiment, therefore, the image density of a registration pattern of each color is adjusted.

Fig. 11 is a flow chart illustrating an example of a first control program to be executed by the image forming apparatus of this embodiment. The flow chart includes Steps S101 to S118.

First at Step S101, the magenta (Ma) image density is set. Specifically, the register 35 shown in Fig. 7 is used for rewriting the contents of the magenta tables of the γ RAMs 34a and 34b. In this embodiment, the video data of 8-bit is used so that each of the γ RAMs has 256 tables. The pattern data is stored at "FFh" so that the highest value in γ RAM is rewritten. In accordance with the infrared reflectivities described above, the highest value in the magenta γ -table is rewritten as "A0h".

Next at Step S102, of the laser beams, two beams for each color, the first laser beam L1 among the laser beams L1 to L4 arranged in this order from the front side along the belt transport direction is made active, and the other laser beam L1' is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the magenta (Ma) registration correction patterns are formed on the transfer belt. The formed patterns are patterns such as shown in Fig. 4. Next, at Step S103, contrary to Step S102, the laser beam L1' is made active and the

laser beam L1 is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the magenta (Ma) registration correction patterns for the second laser beam are formed on the transfer belt.

Similar to forming the magenta (Ma) patterns at Steps S101 to S103, cyan (Cy) patterns are formed at Steps S104 to S106, yellow (Ye) patterns are formed at Steps S107 to S109 and black (Bk) patterns are formed at Steps S110 to S112, respectively on the transfer belt 1.

In setting the image density of each color, the cyan (Cy) image density is set to "C0h", and the yellow (Ye) image density is set to "F0h". Since the carbon black is used as the black (Bk) toner, the pattern image is formed on the yellow (Ye) patch. Therefore, the black image density is set to "FFh" as having the largest infrared absorption value.

More specifically, at Step S104, the cyan (Cy) image density is set. The register 35 shown in Fig. 7 is used for rewriting the contents of the cyan tables of the γ RAMs 34a and 34b to "C0h".

Next at Step S105, the first laser beam L2 is made active, and the other laser beam L2' is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the cyan (Cy) registration correction patterns are formed on the

transfer belt 1. The formed patterns are patterns such as shown in Fig. 4. Next, at Step S106, contrary to Step S105, the laser beam L2' is made active and the laser beam L2 is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the cyan (Cy) registration correction patterns for the second laser beam are formed on the transfer belt 1.

Next, at Step S107, the yellow (Ye) image density is set. The register 35 shown in Fig. 7 is used for rewriting the contents of the yellow tables of the γ RAMs 34a and 34b to "F0h".

Next at Step S108, the first laser beam L3 is made active, and the other laser beam L3' is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the yellow (Ye) registration correction patterns are formed on the transfer belt 1. The formed patterns are patterns such as shown in Fig. 4. Next, at Step S109, contrary to Step S108, the laser beam L3' is made active and the laser beam L3 is set to an off-state or a state that a laser beam intensity is weak such as a bias turn-on state. Then, the yellow (Ye) registration correction patterns for the second laser beam are formed on the transfer belt 1.

Next, at Step S110, the black (Bk) image density is set. The register 35 shown in Fig. 7 is used for

rewriting the contents of the yellow tables of the
γRAMs 34a and 34b to "FFh".

Next at Step S111, the first laser beam L4 is made
active, and the other laser beam L4' is set to an off-
5 state or a state that a laser beam intensity is weak
such as a bias turn-on state. Then, the black (Bk)
registration correction patterns are formed on the
transfer belt 1. The formed patterns are patterns such
as shown in Fig. 4. Next, at Step S112, contrary to
10 Step S111, the laser beam L4' is made active and the
laser beam L4 is set to an off-state or a state that a
laser beam intensity is weak such as a bias turn-on
state. Then, the black (Bk) registration correction
patterns for the second laser beam are formed on the
15 transfer belt 1.

In the above-described manner, the registration
correction pattern forming operation is repeated eight
times to form eight registration correction patterns by
the first laser beams and eight registration correction
20 patterns by the second laser beams on the transfer belt
1, totalling in sixteen patterns (i.e., eight sets). A
plurality of registration correction patterns are
formed in order to suppress a correction miss to be
caused by defective pattern formation and improve the
25 registration correction level.

Next, it is checked at Step S113 whether eight
sets have been completed. If completed, at Step S114

the registration correction patterns of 8×2 formed on the transfer belt are sequentially read to form histogram data which is stored in RAMS 603 and 604 of the registration controller 20.

5 Next at Step S115, the position data of each color registration pattern is calculated. CPU 52c accesses the registration correction pattern image data stored in RAMS 603 and 604 and calculates the peak of the histogram data of each color in the main and sub-
10 scanning directions to obtain the position of each color pattern. Since eight sets of pattern position data are obtained, an average of pattern position data is used as the final pattern position data.

 Each color pattern is read along the belt
15 transport direction at a constant timing corresponding to a twofold of the size of the registration pattern. Each color pattern is also formed at the same timing. Therefore, a shift of the center position of histogram data of each color corresponds to a relative shift
20 amount of the registration pattern of each color.

 At Step S116, relative to the reference color (in this embodiment, cyan (Cy)) of the first laser beam, a registration shift amount of each of other patterns formed by the first and second laser beams is
25 calculated.

 In this embodiment, position read precisions of each color in the main and sub-scanning directions are

set to "256/256" steps. One step has a resolution of
"18 μ m" which corresponds to the precision of the
registration pattern reading sensor. The belt
transport direction is the sub-scanning direction, and
5 the direction perpendicular to the transport direction
is the main scanning direction.

It is assumed herein that the cyan (Cy)
registration pattern formed by the first laser beam on
the right side along the belt transport direction is at
10 the position "128/128" in the main and sub-scanning
directions and that on the left side is at the position
"128/128". In this case, if the magenta (Mg)
registration pattern formed by the first laser beam on
the right side is at the position "136/120" and that on
15 the left side is at the position "120/120", then it is
concluded that the magnification of the magenta (Ma)
pattern relative to the cyan (Cy) pattern formed by the
first laser beams advances by 16 steps (288 μ m) in the
main scanning direction and that the pattern write
20 position advances by 8 steps (144 μ m) in the sub-
scanning direction.

In accordance with the shift amount of each of the
other colors relative to the cyan (Cy) registration
pattern by the first laser beam calculated in the above
25 manner, registration correction data of the
registration patterns by the first and second laser
beams other than the cyan (Cy) registration patterns by

the first laser beam are calculated at Step S117.

In the above-described example, the registration correction data is used for adjusting the write position of the magenta (Mg) first laser beam relative to the cyan (Cy) first laser beam to effect a delay of "144 μ m" in the sub-scanning direction and for adjusting the magnification of the reflection mirror 1000Ma of the laser scan optical system corresponding to "288 μ m" by driving the magnification correction motor M1.

At Step S118, in accordance with the registration correction data calculated at Step S117, electrical correction for the write position of the registration pattern of each color and/or mechanical correction for the laser optical path, respectively in the main and sub-scanning directions, is executed.

In this example, since the inclination is not shifted, the inclination correction motor is not driven. However, if there is any shift of the inclination, the inclination correction is executed similar to the magnification correction.

The first and second laser beams of each color are applied to the photosensitive drum via the common optical system. Therefore, for example, after the magnification of the magenta (Ma) first laser beam is adjusted, the magnification of the second laser beam is not necessary. Namely, after the registration

correction (optical path correction and write position
correction for inclination/magnification adjustment) by
the first laser beams of the colors other than the
first laser beam of cyan (Cy), only the write position
5 adjustment by the second laser beams of the other
colors Ma, Cy, Ye and Bk is executed for the
registration correction.

As described above, in the registration correction
mode of this embodiment, the first and second laser
10 beams of each color are adjusted at the same time, and
the image density of each color is set independently
for each color. The image registration correction can
therefore be made at a good precision by radiating
eight laser beams.

15 In the usual image forming mode, after the
position shift of each color is corrected, the register
35 controls the γ RAMs 34a and 34b to set the common
density value for each color, without rewriting the γ -
tables to set the predetermined density to each color.

20 Also in this embodiment, although the Cy first
laser beam is used as the reference color, the other
colors such as Ma, Ye and Bk of the first laser beam
other than Cy, and Cy, Ma, Ye and Bk of the second
laser beam, may also be used with similar expected
25 advantages.

Also in this embodiment, an image is formed by PWM
modulating the laser beam with video signals. The

power of the laser beam may also be modulated with similar expected advantages.

In this embodiment, the image density of magenta (Ma) is set to "A0h", the image density of cyan (Cy) is set to "C0h", the image density of yellow (Ye) is set to "F0h", and the image density of black (Bk) is set to "FFh". The image densities for magenta (Ma), cyan (Cy), yellow (Ye) and black (Bk) are not limited to the above values, but they may be determined beforehand in accordance with the reflectivities of coloring material components and stored beforehand in ROM or the like.

In this embodiment, although the registration correction mark is formed on the transfer belt, it may be formed on, for example, a recording sheet. If an apparatus is used which transfers an image on a recording sheet via an intermediate transfer member (belt/drum), the registration correction mark may be formed on the intermediate transfer member, with similar expected advantages.

As above, the image density of the registration correction mark formed by each image station is set independently for each color. Therefore, a difference of detection levels between registration correction patterns can be eliminated, which might be caused by a difference of image densities of the patterns formed by the image stations, i.e., a difference of developments, coloring material components and the like. Therefore,

in accordance with the registration shift amount obtained by detecting each registration correction mark formed by each image station, the electrical correction for record image signals and/or mechanical correction for the optical path length and optical path by driving the reflection mirror disposed along the optical path of the laser beam can be executed at high precision.

In this embodiment, the image density of the registration correction pattern of each color is changed by changing the contents of the γ -table of each color in the position shift correction mode. The image density of the registration correction pattern may be changed by changing the power of each laser beam.

Fig. 12 is a block diagram showing the main structure of the video controller 52a of the image processing station 52 which changes the image density of the registration correction pattern by changing the power of each laser beam. In Fig. 12, similar constituent elements to those shown in Fig. 7 are represented by using identical reference numerals and the description thereof is omitted.

Also in the circuit shown in Fig. 12, in the position shift correction mode, i.e., in the mode of forming registration correction patterns and a patch, the pattern image and patch are selected by the selectors 32a and 32b in response to the select signal $\beta 527$, the selected image data $\beta 522a$ and $\beta 522b$ are

supplied to the γ RAMs 34a and 34b, and the γ -transformed image data $\beta 523a$ and $\beta 523b$ is output to the laser drivers 38a and 38b via gate circuits 37a and 37b as the video signals $\beta 525a$ and $\beta 525b$.

5 In this embodiment, the powers of laser beams driven by the laser drivers 38a and 38b can be changed by a control signal $\beta 532$ supplied from the register 35. In the position shift correction mode, the powers of laser beams of each color are changed by the control
10 signal $\beta 532$ when the registration correction patterns are formed. It is therefore possible to change the image density of the registration correction pattern of each color.

 Fig. 13 is a diagram showing the details of the
15 laser drivers 38a and 38b.

 In this embodiment, as described previously, in order to correct the position shift, registration patterns are formed on the transfer belt and the images of these registration correction patterns are read with
20 the CCD sensors. Since the white/black CCD sensor is used, the registration correction pattern formed by color toner is read as an ND image. The read data has therefore a large level difference between colors.

 When the registration correction pattern is read
25 in the visual range, the color level of the transfer belt is also read. Therefore, the dynamic range of the read level of each registration pattern becomes small

and the influence by external disturbance becomes very large. Even if the registration correction pattern is read in the infrared range, the read level of each color is difficult to be made equal because the toners
5 in the infrared range have different reflectivities.

The registration correction pattern formed on the Ye patch is read for the color Bk. It is therefore necessary to increase the image densities of both Ye and Bk, to increase the infrared reflectivity of the Ye
10 patch, and to increase the infrared absorption amount of the Bk pattern.

In this embodiment, separately from the usual image forming mode, only in the position shift correction mode, the power of each laser beam is
15 controlled to control the image density of each registration correction pattern independently for each color.

A PWM generation circuit 381 shown in Fig. 13 generates a PWM signal in accordance with the video
20 signals $\beta 525a$ and $\beta 525b$ output from the gate circuits 37a and 37b shown in Fig. 12 and video clocks $\beta 530a$ and $\beta 530b$ synchronizing with the video signals.

Specifically, the PWM generation circuit 381 generates a reference triangular wave shown in Fig. 14A
25 in accordance with the video clock $\beta 530$, and compares the reference triangular wave with the D/A converted video signal $\beta 525$.

If the level of the video signal is large (near to FFh), the high level period of the PWM signal becomes long, and in the opposite case, the high level period becomes short. Therefore, the gradation of an image is controlled by controlling the turn-on period of a laser beam during one video clock period in accordance with the level of the video signal. The image density can therefore be controlled by adjusting the laser beam turn-on period in accordance with the video signal level.

The level of the video signal may be controlled to adjust the image density of the registration correction pattern. However, if the image density of the registration correction pattern is changed by controlling the turn-on period of the laser beam, the area of each pixel in the registration correction pattern changes with the set image density, which may pose a problem in reading the registration correction image.

In order to solve this problem, in this embodiment, in the position shift correction mode, the resistor 35 controls the γ -tables 34a and 34b to fix the level of an output video signal to "FFh". In addition, the laser control signal $\beta 532$ output from the register 35 makes the illumination light amount of a laser beam variable to thereby control the image density of the registration correction pattern. As the

laser light amount increases, the latent image on the photosensitive drum becomes deep so that the amount of developed toner increases and the thickness of a developed image becomes large without changing the width of the image. Namely, the toner amount per unit area increases and the reflection light amount increases.

More specifically, in the position shift correction mode, laser output setting values $\beta 531a$ and $\beta 531b$ set by the register 35 are converted by a D/A converter 382 into analog laser output setting values $\beta 532a$ and $\beta 532b$. A laser drive unit 383 has a constant current source for driving the laser which is turned on and off by a PWM signal under a constant current. Only in the position shift correction mode, the set value of this constant current source is adjusted for each color to change the emission light amount of the laser.

In this embodiment, a laser chip of about 10 mW is used, and the constant current value is made adjustable in the range of 60 mA to 80 mA as shown in Fig. 14C.

In this embodiment, in accordance with the reflectivities of toners in the infrared range, the image densities are set so as to satisfy $Bk > Ye > (\geq) Cy > (\geq) Ma$. In the usual image forming mode, the laser emission amount is set equal for all colors, and only when registration correction patterns are formed, the laser emission amount is changed for each color.

The reflectivity of each toner does not change greatly with time and temperature. Therefore, a predetermined laser output setting value corresponding to each reflectivity is read from the register 35 to
5 set the image density. The color Bk has a largest difference of the reflectivity relative to the other colors. In this connection, a Bk registration correction pattern may be formed at a first image density, and the registration correction patterns of
10 other three colors excepting Bk may be formed at a second image density.

In the embodiment shown in Figs. 12 to Figs. 14A, 14B and 14C, in the position shift correction mode, the emission amount of a laser beam of each color is
15 controlled to change the image density of each color when the registration correction patterns are formed. It is possible to make the reflection light amount of each color equal, to improve a read precision of the registration correction pattern, and to correct the
20 position shift with a high precision.

The structure of data processing programs readable by the image forming apparatus of this invention will be described with reference to a memory map shown in Fig. 15.

25 Fig. 15 is a memory map of a storage medium storing various data processing programs readable by the image forming apparatus of this invention.

Although not shown in Fig. 15, other data may be stored in the storage medium. Such data includes management data for programs stored in the storage medium such as version information and programmer names, and data dependent on OS reading a program such as icons for discriminating programs.

Data used by each of various programs is also managed by the directory shown in Fig. 15. If programs and data to be installed are being compressed, a decompressing program and the like may also be stored.

A host computer running on an externally installed program may realize the functions of the embodiment illustrated in Fig. 11. In this case, the invention is applicable to the case wherein a data set including the program is supplied to an output apparatus by using a storage medium such as a CD-ROM, a flash memory and an floppy disc or an external storage medium via a network.

It is obvious that the objects of the invention can be achieved by supplying a storage medium storing software program codes realizing the functions of each embodiment described above to a system or an apparatus whose computer (CPU or MPU) reads and executes the program codes stored in the storage medium.

In this case, the software program codes themselves realize the novel functions of the invention. Therefore, the storage medium storing the

program codes constitutes the present invention.

The storage medium for storing such program codes may be a floppy disk, a hard disk, an optical disk, a magnetooptical disk, a CD-ROM, a CD-R, a DVD-ROM, a magnetic tape, a nonvolatile memory card, a ROM, an EEPROM, a silicon disc or the like.

It is obvious that the scope of the invention contains not only the case wherein the functions of the embodiment are realized by a CPU reading and executing the program codes but also the case wherein the functions of the embodiment are realized by an OS (operating system) or the like running on a computer and executing a portion or the whole of actual processes in accordance with the program codes.

It is obvious that the scope of the invention also contains the case wherein the functions of each embodiment can be realized by writing the program codes into a memory of a function expansion board inserted into a computer or of a function expansion unit connected to the computer, and thereafter by executing a portion or the whole of actual processes by a CPU of the function expansion board or function expansion unit.

The invention is also applicable to a system having a plurality of apparatuses or to a single apparatus. It is obvious that the invention is applicable to the case wherein the embodiment functions

are realized by supplying programs to the system or apparatus. In this case, the system or apparatus can receive the advantageous effects of the invention by reading the storage medium storing the software programs realizing the embodiment functions.

The system or apparatus can receive the advantageous effects of the invention by downloading the software program codes realizing the embodiment functions from a database on a network by using a communication program and reading the software program codes.

Many widely different embodiments of the present invention may be constructed without departing from the spirit and scope of the present invention. It should be understood that the present invention is not limited to the specific embodiments described in the specification, except as defined in the appended claims.